Instructor: Dr. Sin Ming Loo  
Office: MEC 202E, Phone: (208) 426-5679, Email: smloo@boisestate.edu  
Office Hours: by appointment

Catalog Description:
Number systems, Boolean algebra, logic gates, Karnaugh mapping, combinatorial circuits, flip-flops, registers, counters, sequential state-machines. Construction of small design projects.

Required text:

Text:

Simulation Software:
Altera Quartus II 4.0

Class/Laboratory Schedule: Lecture/Discussion: 6 hours/week

Time and Place: (Lecture) MW 6PM to 9PM, MEC 114

Course web page:
http://coen.boisestate.edu/smloo/ee230summer2004

Topics:
1. Number Systems and Codes
2. Combinational Logic Design Principles
3. Combinational Logic Design Practices
4. Sequential Logic Design Principles
5. Sequential Logic Design Practices
6. Memory, CPLDs, and FPGAs

Prerequisites:
COMPSCI 117 or COMPSCI 125

Grading:
50-Minute Exams (2 @ 20% each) 40%
Final Exam 20%
Quiz 20%
Homework (includes PSpice Simulations) 20%

Grade determination: 100%-90% = A, 89%-80% = B, 79%-70% = C, 69%-60% = D, < 60% = F

Note and disclaimers:
There will be rough spots. Question and comments are expected and encouraged.

Homework:
Homework will be assigned on Wednesday and due at 5pm of the following Wednesday. NO LATE homework will be accepted.
**Code of Conduct:**
Discussing the assignments with other students is encouraged, as this could be one way to understand the materials. However, the work submitted must be your own. Copying from any source (from someone else, old files, or solution manual!) and turning it in is not permitted. Penalties for copying/cheating range from receiving a 0 on the assignment to receiving an F for the course.

*Student Code of Conduct, Article 3, Section 1, Academic Dishonesty*
Cheating or plagiarism in any form is unacceptable. The University functions to promote the cognitive and psychosocial development of all students. Therefore, all work submitted by a student must represent her/his own ideas, concepts, and current understanding. Academic dishonesty also includes submitting substantial portions of the same academic course work to more than one course for credit without prior permission of the instructor(s).

**Course Objectives:**
After taking this course, the students should be able:
- Calculate the functions described by logic diagrams.
- Implement functions in logic gates.
- Design and evaluate circuits to form basic digital circuits.
- Develop proficiency in using Karnaugh maps, functional reduction, and data sheets.
- Design circuits using state diagrams.
- Develop circuits using combinational and clocked logic.